SPECIFICATION AMENDMENTS

Please amend as follows paragraph [0011], that is, the first paragraph of the Summary of the Invention section of the specification:

The invention provides a method for performing an input/output (I/O) operation in a computer between an I/O-initiating subsystem and a device through a <u>hardware</u> memory, in which the memory is arranged into portions that are separately addressable using first identifiers that are represented using a first number of <u>address</u> bits; for the I/O operation, the device accesses a first space of the memory <u>using a second number of address bits</u>, and the subsystem addresses I/O requests to a second space of the memory using second identifiers that are represented using a second number of bits. The second identifiers are initially mapped to respective first identifiers that identify portions of the memory in the second memory space. For any I/O request that meets a remapping criterion, the corresponding second identifier is remapped to one of the first identifiers that identifies a portion of the memory in the first space of the memory. The second space is different from the first space and the second number of <u>address</u> bits is greater <u>less</u> than the first number of <u>address</u> bits.

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